



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

ProLiant DL580 Gen8  
(3.20 GHz, Intel Xeon E7-8891 v2)

**SPECint®\_rate2006 = 1800**

**SPECint\_rate\_base2006 = 1740**

**CPU2006 license:** 3

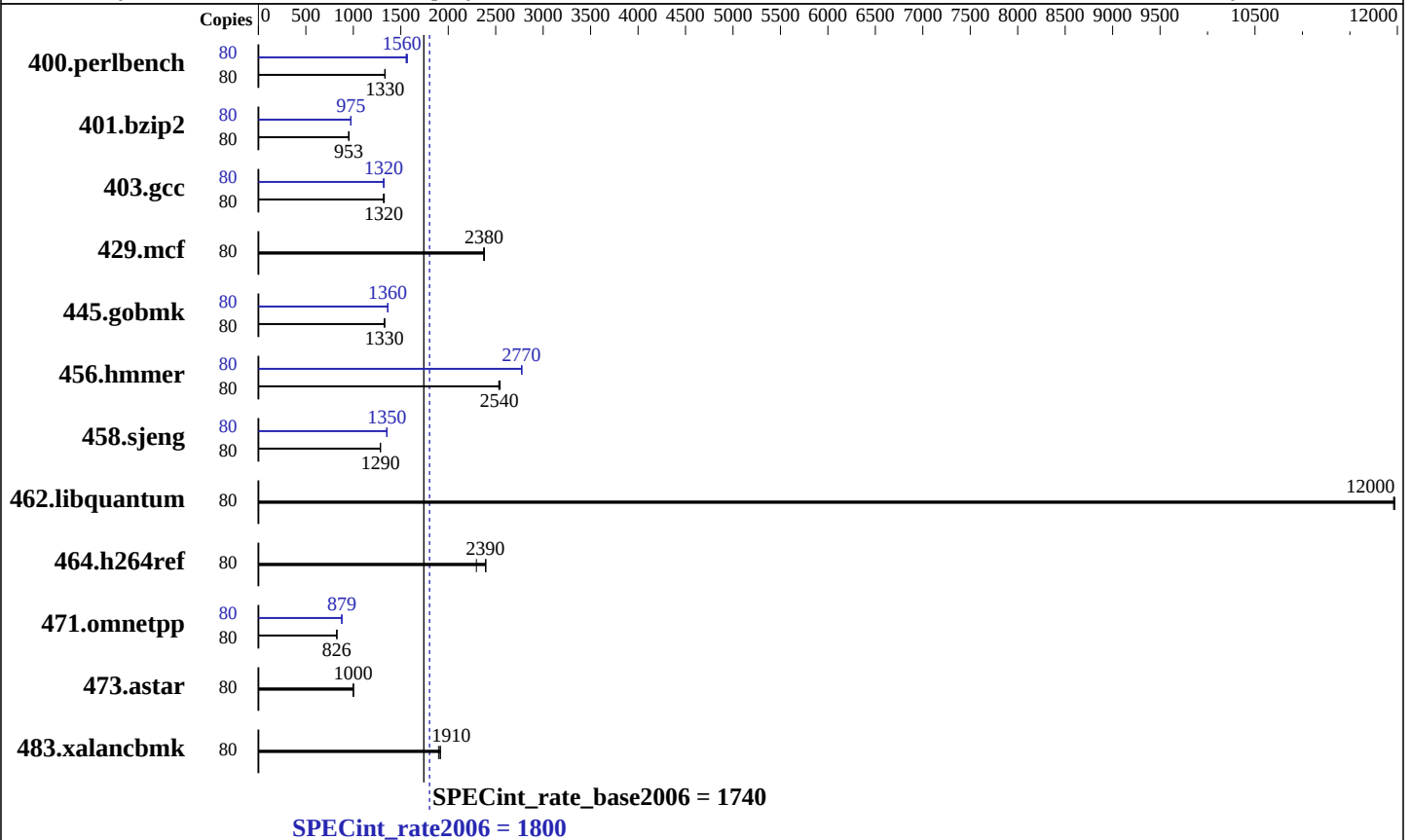
**Test sponsor:** Hewlett-Packard Company

**Tested by:** Hewlett-Packard Company

**Test date:** Apr-2014

**Hardware Availability:** Feb-2014

**Software Availability:** Nov-2013



### Hardware

**CPU Name:** Intel Xeon E7-8891 v2  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
**CPU MHz:** 3200  
**FPU:** Integrated  
**CPU(s) enabled:** 40 cores, 4 chips, 10 cores/chip, 2 threads/core  
**CPU(s) orderable:** 2,4 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 37.5 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 1 TB (64 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz and CL9)  
**Disk Subsystem:** 1 x 400 GB SSD SAS, RAID 0  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 6.5, (Santiago)  
Kernel 2.6.32-431.el6.x86\_64  
**Compiler:** C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
**Auto Parallel:** No  
**File System:** ext4  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.0



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## Results Table

| Benchmark      | Base   |            |             |            |             |            |              | Peak   |            |             |            |             |            |              |
|----------------|--------|------------|-------------|------------|-------------|------------|--------------|--------|------------|-------------|------------|-------------|------------|--------------|
|                | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio        | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio        |
| 400.perlbench  | 80     | <b>586</b> | <b>1330</b> | 587        | 1330        | 585        | 1340         | 80     | 502        | 1560        | <b>501</b> | <b>1560</b> | 498        | 1570         |
| 401.bzip2      | 80     | 810        | 953         | 811        | 951         | <b>810</b> | <b>953</b>   | 80     | 792        | 975         | 794        | 972         | <b>792</b> | <b>975</b>   |
| 403.gcc        | 80     | 486        | 1330        | 489        | 1320        | <b>488</b> | <b>1320</b>  | 80     | 489        | 1320        | <b>487</b> | <b>1320</b> | 487        | 1320         |
| 429.mcf        | 80     | <b>307</b> | <b>2380</b> | 306        | 2380        | 307        | 2370         | 80     | <b>307</b> | <b>2380</b> | 306        | 2380        | 307        | 2370         |
| 445.gobmk      | 80     | <b>631</b> | <b>1330</b> | 631        | 1330        | 632        | 1330         | 80     | <b>615</b> | <b>1360</b> | 615        | 1360        | 616        | 1360         |
| 456.hammer     | 80     | 293        | 2550        | <b>294</b> | <b>2540</b> | 295        | 2530         | 80     | <b>269</b> | <b>2770</b> | 269        | 2780        | 269        | 2770         |
| 458.sjeng      | 80     | 753        | 1290        | <b>753</b> | <b>1290</b> | 754        | 1280         | 80     | 714        | 1360        | 717        | 1350        | <b>715</b> | <b>1350</b>  |
| 462.libquantum | 80     | 138        | 12000       | 139        | 12000       | <b>139</b> | <b>12000</b> | 80     | 138        | 12000       | 139        | 12000       | <b>139</b> | <b>12000</b> |
| 464.h264ref    | 80     | 771        | 2300        | <b>740</b> | <b>2390</b> | 738        | 2400         | 80     | 771        | 2300        | <b>740</b> | <b>2390</b> | 738        | 2400         |
| 471.omnetpp    | 80     | 605        | 826         | 605        | 826         | <b>605</b> | <b>826</b>   | 80     | 569        | 878         | 569        | 879         | <b>569</b> | <b>879</b>   |
| 473.astar      | 80     | <b>561</b> | <b>1000</b> | 561        | 1000        | 562        | 999          | 80     | <b>561</b> | <b>1000</b> | 561        | 1000        | 562        | 999          |
| 483.xalancbmk  | 80     | 291        | 1900        | 288        | 1920        | <b>288</b> | <b>1910</b>  | 80     | 291        | 1900        | 288        | 1920        | <b>288</b> | <b>1910</b>  |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
Disabled unused Linux services through "stop\_services.sh" before running.

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Maximum Performance  
Collaborative Power Control set to Disabled  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to Disabled

Sysinfo program /cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on DL580-Gen8-SR Mon Apr 21 15:53:13 2014

Continued on next page



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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E7-8891 v2 @ 3.20GHz
 4 "physical id"s (chips)
 80 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings : 20
physical 0: cores 2 3 4 5 6 7 8 10 11 12
physical 1: cores 2 3 4 5 6 7 8 10 11 12
physical 2: cores 2 3 4 5 6 7 8 10 11 12
physical 3: cores 2 3 4 5 6 7 8 10 11 12
```

cache size : 38400 KB

From /proc/meminfo

```
MemTotal: 1058653984 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux DL580-Gen8-SR 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 21 15:48

SPEC is set to: /cpu2006

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda3  | ext4 | 365G | 13G  | 334G  | 4%   | /          |

Additional information from dmidecode:

BIOS HP P79 02/21/2014

Memory:

```
64x HP 712383-081 16 GB 1333 MHz 2 rank
32x UNKNOWN NOT AVAILABLE
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 1 TB and the dmidecode description should have one line reading as:

Continued on next page

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info@spec.org  
<http://www.spec.org/>

Page 3



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## Platform Notes (Continued)

64x HP 712383-081 16 GB 1333 MHz 2 rank

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl, -z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -wl, -z, muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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