



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 118

Servidor Itautec LX103 (Intel Xeon X3450)

SPECint_rate_base2006 = 110

CPU2006 license: 9001

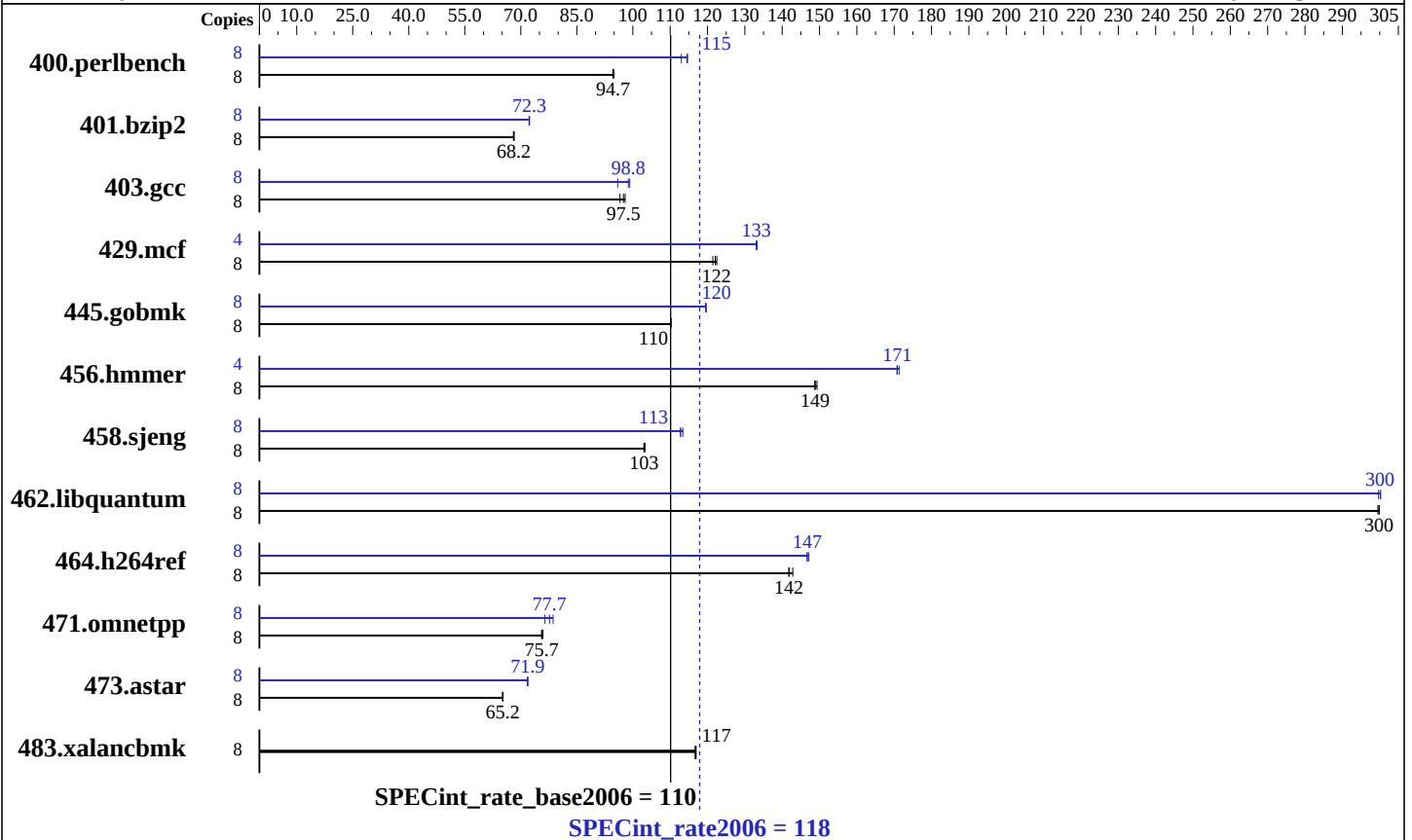
Test sponsor: Itautec

Tested by: Itautec

Test date: Mar-2011

Hardware Availability: Dec-2009

Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon X3450
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (4 x 2 GB 2Rx8 PC3-10600U-9, ECC)
Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l_cproc_p_11.1.072
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	826	94.7	824	94.9	<u>825</u>	<u>94.7</u>	8	692	113	682	115	<u>683</u>	<u>115</u>
401.bzip2	8	1132	68.2	1135	68.0	<u>1133</u>	<u>68.2</u>	8	1069	72.2	1067	72.4	<u>1068</u>	<u>72.3</u>
403.gcc	8	658	97.9	<u>661</u>	<u>97.5</u>	667	96.6	8	671	95.9	649	99.2	<u>652</u>	<u>98.8</u>
429.mcf	8	601	121	<u>598</u>	<u>122</u>	596	122	4	274	133	<u>274</u>	<u>133</u>	274	133
445.gobmk	8	<u>762</u>	<u>110</u>	761	110	762	110	8	<u>702</u>	<u>120</u>	702	119	702	120
456.hammer	8	<u>501</u>	<u>149</u>	500	149	502	149	4	219	171	218	171	<u>218</u>	<u>171</u>
458.sjeng	8	940	103	<u>940</u>	<u>103</u>	937	103	8	858	113	<u>858</u>	<u>113</u>	854	113
462.libquantum	8	553	300	553	300	<u>553</u>	<u>300</u>	8	552	300	<u>552</u>	<u>300</u>	553	300
464.h264ref	8	1249	142	1239	143	<u>1248</u>	<u>142</u>	8	<u>1206</u>	<u>147</u>	1203	147	1207	147
471.omnetpp	8	662	75.6	<u>661</u>	<u>75.7</u>	658	75.9	8	636	78.7	<u>644</u>	<u>77.7</u>	654	76.5
473.astar	8	861	65.3	863	65.1	<u>861</u>	<u>65.2</u>	8	781	71.9	783	71.7	<u>781</u>	<u>71.9</u>
483.xalancbmk	8	473	117	<u>472</u>	<u>117</u>	472	117	8	473	117	<u>472</u>	<u>117</u>	472	117

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

This result was measured on the Servidor Itaotec LX114.
The Servidor Itaotec LX103, Servidor Itaotec LX113 and the Servidor Itaotec LX114 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -w1, -z, muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -w1, -z, muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):

-L/opt/sh/SmartHeap_8/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.xml>

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For other inquiries, please contact webmaster@spec.org.

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