



SPEC[®] CINT2006 Result

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Dell Inc.

SPECint[®]_rate2006 = 260

Dell Precision T7500 (Intel Xeon W5590, 3.33 GHz)

SPECint_rate_base2006 = 245

CPU2006 license: 55

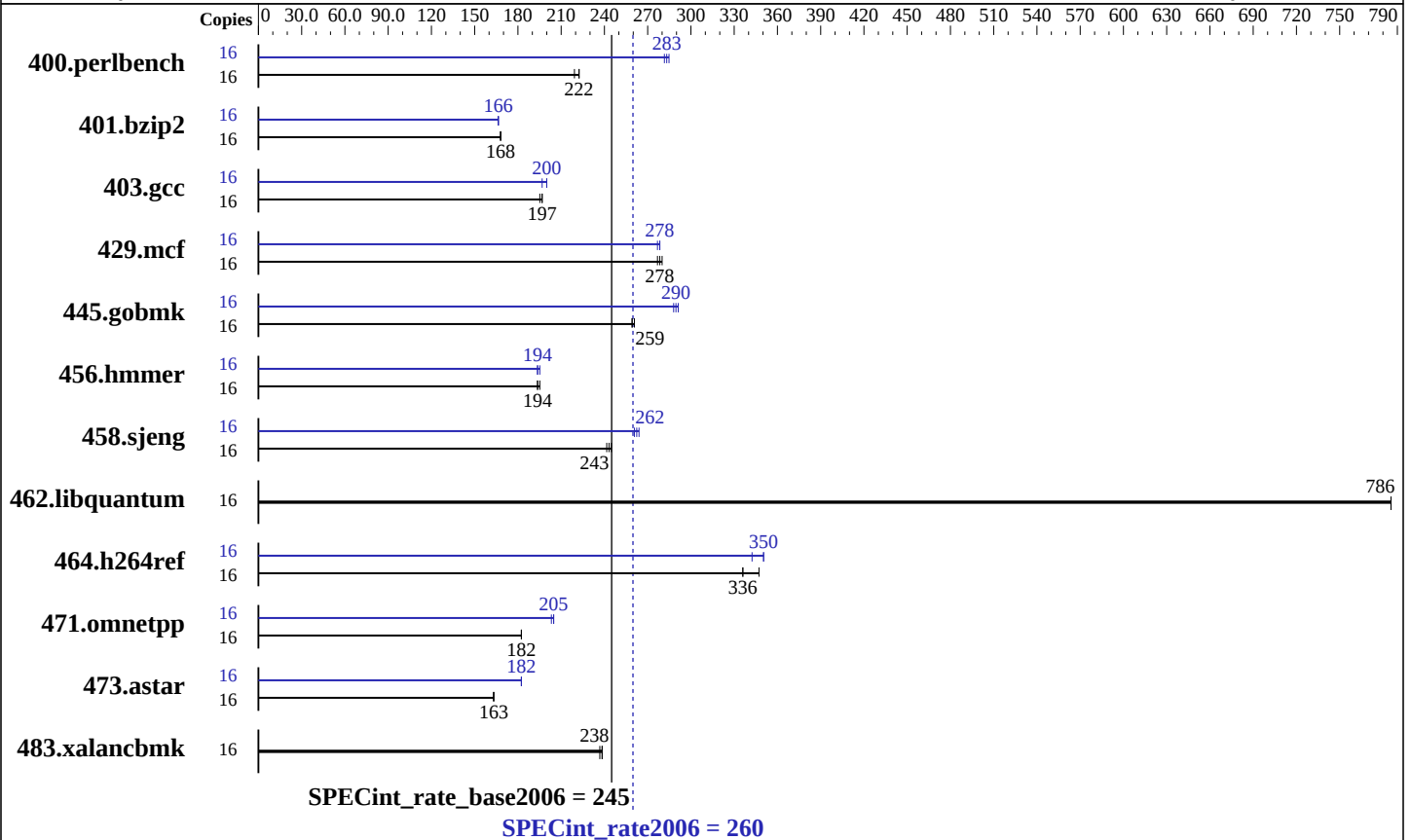
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Aug-2009

Hardware Availability: Aug-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon W5590
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 3333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12x4 GB PC3-10600R)
Disk Subsystem: 1 x 146 GB SAS 15000 RPM
Other Hardware: None

Software

Operating System: Windows Vista Business SP1 (64-bit)
Compiler: Intel C++ Compiler for IA-32, Version 11.0
Build 20090131 Package ID: w_cproc_p_11.0.072
Microsoft Visual Studio 2008 SP1
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	715	219	702	222	<u>705</u>	<u>222</u>	16	550	285	<u>553</u>	<u>283</u>	554	282
401.bzip2	16	920	168	922	168	<u>921</u>	<u>168</u>	16	925	166	<u>927</u>	<u>166</u>	929	166
403.gcc	16	656	197	661	195	<u>657</u>	<u>197</u>	16	645	200	657	197	<u>646</u>	<u>200</u>
429.mcf	16	<u>525</u>	<u>278</u>	522	280	527	277	16	524	278	527	277	<u>525</u>	<u>278</u>
445.gobmk	16	<u>646</u>	<u>259</u>	646	259	644	261	16	577	291	<u>579</u>	<u>290</u>	583	288
456.hammer	16	765	195	770	194	<u>768</u>	<u>194</u>	16	<u>769</u>	<u>194</u>	771	194	762	195
458.sjeng	16	792	245	799	242	<u>798</u>	<u>243</u>	16	740	261	<u>739</u>	<u>262</u>	733	264
462.libquantum	16	422	786	<u>422</u>	<u>786</u>	422	786	16	422	786	<u>422</u>	<u>786</u>	422	786
464.h264ref	16	1055	336	1018	347	<u>1054</u>	<u>336</u>	16	1009	350	<u>1010</u>	<u>350</u>	1032	342
471.omnetpp	16	<u>550</u>	<u>182</u>	550	182	549	182	16	491	203	<u>489</u>	<u>205</u>	489	205
473.astar	16	690	163	<u>690</u>	<u>163</u>	691	163	16	<u>615</u>	<u>182</u>	616	182	615	182
483.xalancbmk	16	464	238	465	237	<u>465</u>	<u>238</u>	16	464	238	465	237	<u>465</u>	<u>238</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Platform Notes

BIOS Settings
Memory Node Interleaving: NUMA
Hyper-Threading: ENABLE

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000

403.gcc: -Qprof_gen(pass 1) -QxSSE4.2 -Qprof_use(pass 2) -Qipo -O3
-Qprec-div- -Qopt-prefetch /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
/F512000000

458.sjeng: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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