



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = Not Run

ACTINA SOLAR 200 S4+ (Intel Xeon E5520)

SPECint_rate_base2006 = 185

CPU2006 license: 9008

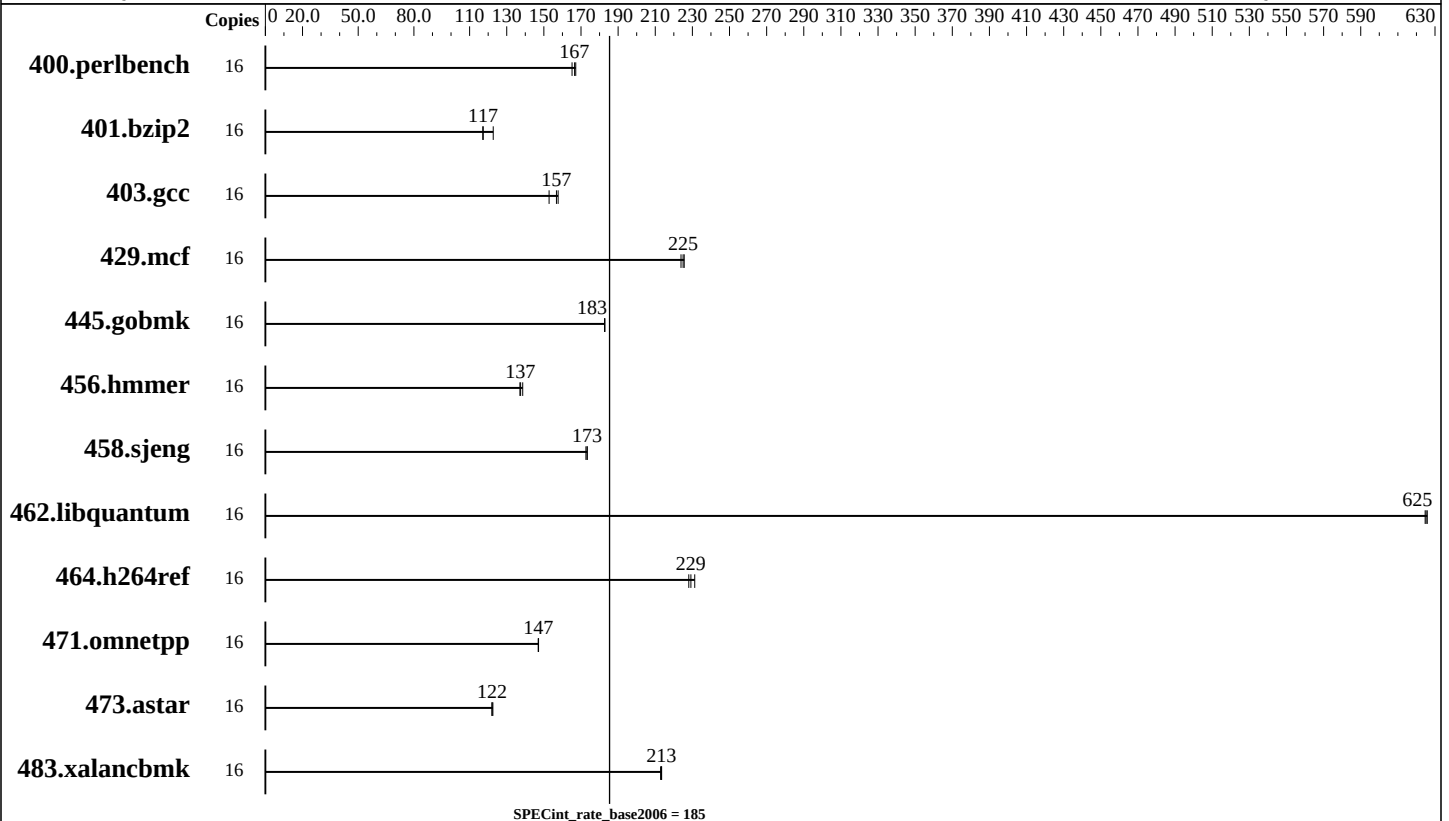
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Aug-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5520
CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB PC3-8500, 1066 MHz, DDR3, ECC)
Disk Subsystem: 500 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502 Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>939</u>	<u>167</u>	946	165	935	167							
401.bzip2	16	1258	123	<u>1316</u>	<u>117</u>	1319	117							
403.gcc	16	<u>822</u>	<u>157</u>	843	153	817	158							
429.mcf	16	<u>649</u>	<u>225</u>	647	226	652	224							
445.gobmk	16	919	183	<u>919</u>	<u>183</u>	918	183							
456.hammer	16	<u>1086</u>	<u>137</u>	1089	137	1077	139							
458.sjeng	16	<u>1117</u>	<u>173</u>	1117	173	1121	173							
462.libquantum	16	531	625	<u>530</u>	<u>625</u>	530	626							
464.h264ref	16	1531	231	1553	228	<u>1545</u>	<u>229</u>							
471.omnetpp	16	680	147	<u>680</u>	<u>147</u>	681	147							
473.astar	16	<u>920</u>	<u>122</u>	917	123	920	122							
483.xalancbmk	16	519	213	<u>517</u>	<u>213</u>	517	213							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores

General Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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